

HARDWARE CHARACTERISTICS	UNIT	UltimET light PCI	UltimET light PCIe	UltimET light TCP/IP
Input voltage	VDC	3.3 ±9%	3.3 ±9%	N/A as embedded in the
Power input	W	6	7	AccurET controller
Compatibility	-	PCI standard 2.1 and 2.2	PCIe, gen1, 2.5 Gbps, x1	N/A
Weight	g	92 ±2	92 ±2	94 ±2

CONTROL FEATURES	UltimET light interpolation		UltimET light smart gateway	
	Movement types	Synchronized and interpolated mvts		Only synchronized mvts
Available hardware formats	PCI / PCIe	TCP/IP embedded in the AccurET	PCI / PCIe	TCP/IP embedded in the AccurET
Number of supported interpolation group	2		N/A	
Max number of DOFs (degrees of freedom) per interpolation group	4 (X, Y, Z, Theta)		N/A	
Interpolation movement types	G-code, PVT, PT, ULM and kinematic transform functionalities		N/A	
On board digital I/Os	4 In / 4 Out	2 In / 2 Out	4 In / 4 Out	2 In / 2 Out
Connectivity with external I/O module	Wago - Modbus on TCP/IP: analog: 16 In / 16 Out and digital: 128 In / 128 Out			
TransnET real-time communication bus	1 Gbps			
TransnET cycle time	100 µs			
Number of supported axes on TransnET	Up to 63			
Traces with pre-/post-trigger function	Yes			
Structured programming language sequences	3 threads of structured programming language sequences			
Master/slave data exchange	Real-time at 10kHz (including slave to slave communication capability)			

